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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10016863	FILING DATE 12/14/2001	CLASS 714	SUBCLASS 718	GAU 2133	EXAMINER <i>Decady</i>
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**APPLICANTS: Kuhn Justus; Weitz Peter;
RSR

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:
GERMANY 100 62 404.9 12/14/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

W&B-INF-908

TITLE : Device and method for reducing the number of addresses of faulty memory cells

U.S. DEPT. OF COMM / PAT & TM-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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☐ CD-ROM

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